Numascale

Numascale is a European SME specialising in interconnects for high performance and enterprise computing. The differentiator for Numascale’s interconnect are the shared memory and cache coherency mechanisms. These features allow programs to access any memory location and any memory mapped I/O device in a multiprocessor system with a high degree of efficiency. It provides scalable systems with a unified programming model that stays the same from the small multi-core machines used in laptops and desktops to the largest imaginable single system image machines that may contain thousands of processors. The architecture is commonly classified as ccNUMA or NUMA but the interconnect system can alternatively be used as a low latency clustering interconnect. The technology comes as an add on card to standard servers providing a large shared memory at cluster price.

Memory Bandwidth*

- Memory bandwidth is essential for many scientific applications
- Every processor has its own memory and adds to the total memory bandwidth
- Stream [1] is a standard benchmark to evaluate the memory bandwidth of a system

```
<table>
<thead>
<tr>
<th>Bandwidth in GB/s</th>
<th>Number of Boards</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>8</td>
</tr>
<tr>
<td>500</td>
<td>16</td>
</tr>
<tr>
<td>1000</td>
<td>24</td>
</tr>
<tr>
<td>1500</td>
<td>32</td>
</tr>
<tr>
<td>2000</td>
<td>40</td>
</tr>
<tr>
<td>2500</td>
<td>48</td>
</tr>
</tbody>
</table>
```

Optimizations for Large NUMA Systems

- Reduced Synchronization Overhead:
  - local data copies where possible
  - local buffers to synchronize writes
  - multithreaded memory allocator, kmp_malloc
  - thread local random number generator
- NUMA aware memory placement
- Since the length of the trajectories differs, load imbalance occurs. The Adaptive NUMA Scheduler is used to handle load imbalance and maintain as much data locality as possible

Performance Results*

```
<table>
<thead>
<tr>
<th>Runtime in hours</th>
<th>Number of Threads</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>24</td>
</tr>
<tr>
<td>2</td>
<td>48</td>
</tr>
<tr>
<td>4</td>
<td>96</td>
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<tr>
<td>6</td>
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<td>8</td>
<td>384</td>
</tr>
<tr>
<td>10</td>
<td>768</td>
</tr>
<tr>
<td>12</td>
<td>1024</td>
</tr>
</tbody>
</table>
```

Runtime and Speedup of Trajsearch with an increasing number of threads.

The Oslo System

The University of Oslo’s Center for Information Technology, UiSIT feature this Numascale installation. The systems is a PRACE prototype and has been used for some of the work between Numascale and RWTH Aachen University.

- 72 IBM x3755 M3 nodes
- 144 AMD Opteron CPUs
- 1728 Cores
- 4.5 TB Memory
- 3D Torus Interconnect (3x6x4)

TrajSearch

TrajSearch is a code to investigate turbulences which occur during combustion. It is a post-processing code for dissipation element analysis developed by Peters and Wang ([2]). It decomposes a highly resolved three dimensional turbulent flow field obtained by Direct Numerical Simulation (DNS) into non-arbitrary, space-filling and non-overlapping geometrical elements called ‘dissipation elements’. Starting from every grid point in the direction of ascending and descending gradient of an underlying diffusion controlled scalar field, a local maximum, respectively minimum point is found. A dissipation element is defined as a volume from which all trajectories reach the same minimum and maximum point. The dissipation element analysis provides a deeper understanding of turbulence and can be employed to reconstruct important statistical properties as has been shown by Gampert and Göbbert ([3]).

The application TrajSearch was optimized to run on large NUMA systems by reducing synchronization, optimizing the memory layout and using the Adaptive NUMA Scheduler for work scheduling. Overall the code delivers a speedup of 625 running with 1024 threads.

**Summary**

- The investigated Numascale system combines 1728 cores and 4.5 TB of main memory in a shared memory machine
- The accumulated memory bandwidth increases with the number of nodes to over 2 TB/s
- The application TrajSearch was optimized to run on large NUMA systems by reducing synchronization, optimizing the memory layout and using the Adaptive NUMA Scheduler for work scheduling

References


*Executables were compiled with the Oracle Solaris Studio 12.3 Compiler.

Dirk Schmidl, RWTH Aachen University
schmidl@itc.rwth-aachen.de

Atle Vesterkjær, Numascale AS
av@numascale.com